

DATA SHEET

Part No.	AN77L06M
Package Code No.	HSIP003-P-0000Q

Maintenance/Discontinued includes following lifecycle stage.
planned maintenance type
maintenance type
planned discontinued type
discontinued type
Please visit following URL about latest information.
<http://www.semicon.panasonic.co.jp/en/>

DATA SHEET

Part No.	AN77L06M
Package Code No.	HSIP003-P-0000Q

Maintenance/Discontinued includes following lifecycle stage.
planned maintenance type
maintenance type
planned discontinued type
discontinued type
Please visit following URL about latest information.
<http://www.semicon.panasonic.co.jp/en/>

Contents

■ Overview	3
■ Features	3
■ Applications	3
■ Package	3
■ Type	3
■ Block Diagram	4
■ Pin Descriptions	5
■ Absolute Maximum Ratings	6
■ Operating supply voltage range	6
■ Electrical Characteristics	7
■ Electrical Characteristics (Reference values for design)	8
■ Technical Data	9
■ Usage Note	10

AN77L06M

3-pin, positive output, low dropout voltage regulator (100 mA type)

■ Overview

The AN77LxxM series are 3-pin, fixed positive output type monolithic voltage regulators. The AN77L06M is the 6 V output voltage type in these series. They can be used widely in power supply circuits with current capacity of up to 100 mA. It is suitable for the low-voltage equipment using batteries and consumer/industrial equipment with great fluctuation of the supply voltage.

Stabilized fixed output voltage is obtained from unstable DC input voltage with using a 10 μ F output capacitor.

11 types of fixed output voltage are available; 3 V, 3.5 V, 4 V, 4.5 V, 5 V, 6 V, 7 V, 8 V, 9 V, 10 V and 12 V.

■ Features

- Minimum input/output voltage difference: 0.25 V typ.
- Built-in over current limit circuit
- Built-in rush current prevention circuit at input voltage rise
- Built-in overheat protection circuit
- Built-in input short-circuit protection circuit

■ Applications

- 3-pin positive output voltage regulator (100 mA type)

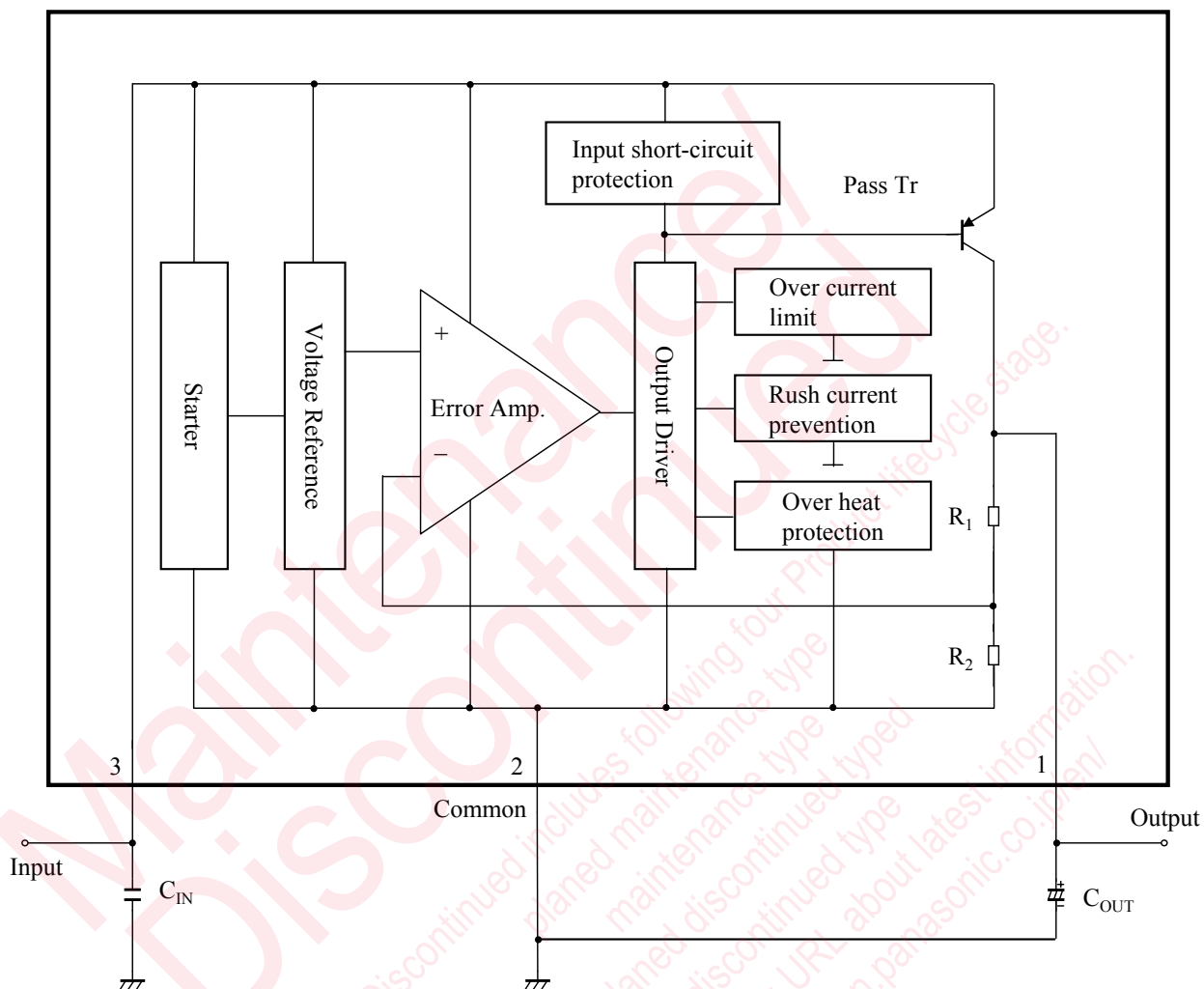
■ Package

- 3-pin Plastic Single Inline Package With Heat Sink (SIP type)

■ Type

- Silicon monolithic bipolar IC

■ Block Diagram



C_{IN} : 0.33 μ F

C_{OUT} : 10 μ F

R_1 : 38 k Ω

R_2 : 5 k Ω

■ Pin Descriptions

Pin No.	Pin name	Type	Description
1	Output	Output	Output voltage
2	Common	GND	Ground.
3	Input	Input	Input voltage

Maintenance/Discontinued includes four Product lifecycle stage.
planned maintenance type
maintenance type
planned discontinued type
discontinued type
Please visit following URL about latest information.
<http://www.semicon.panasonic.co.jp/en/>

■ Absolute Maximum Ratings

A No.	Parameter	Symbol	Rating	Unit	Note
1	Input voltage	V_{IN}	30	V	*1
2	Supply current	I_{CC}	200	mA	—
3	Power dissipation	P_D	252	mW	*2
4	Operating ambient temperature	T_{opr}	-30 to +85	°C	*3
5	Storage temperature	T_{stg}	-55 to +150	°C	*3

Note) *1: The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

*2: The power dissipation shown is the value at $T_a = 85^\circ\text{C}$.

When using this IC, refer to the • P_D - T_a diagram in the ■ Technical Data and use under the condition not exceeding the allowable value.

When T_j exceeds 150°C , the internal circuit cuts off the output.

*3: Except for the power dissipation, operating ambient temperature, and storage temperature, all ratings are for $T_a = 25^\circ\text{C}$.

■ Operating supply voltage range

Parameter	Symbol	Range	Unit	Note
Supply voltage range	V_{CC}	$(V_{OUT} + 0.46 \text{ V})$ to 16.74	V	—

Note) The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

■ Electrical Characteristics

Note) Unless otherwise specified, $T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$, $V_{\text{IN}} = 7\text{ V}$, $I_{\text{OUT}} = 50\text{ mA}$, $C_{\text{IN}} = 0.33\text{ }\mu\text{F}$ and $C_{\text{OUT}} = 10\text{ }\mu\text{F}$.

B No.	Parameter	Symbol	Conditions	Limits			Unit	Note
				Min	Typ	Max		
1	Output voltage	V_{OUT}	—	5.76	6.0	6.24	V	—
2	Line regulation	REG_{IN}	$V_{\text{IN}} = 6.74\text{ V to } 16.74\text{ V}$	—	4.0	60	mV	—
3	Load regulation	REG_{L}	$I_{\text{OUT}} = 0\text{ mA to } 100\text{ mA}$	—	11	60	mV	—
4	Minimum input/output voltage difference 1	$V_{\text{DIF}(\text{min})1}$	$V_{\text{IN}} = 5.4\text{ V}$, $I_{\text{OUT}} = 50\text{ mA}$	—	0.12	0.25	V	—
5	Minimum input/output voltage difference 2	$V_{\text{DIF}(\text{min})2}$	$V_{\text{IN}} = 5.4\text{ V}$, $I_{\text{OUT}} = 100\text{ mA}$	—	0.25	0.46	V	—
6	Bias current before regulation start	I_{rush}	$V_{\text{IN}} = 5.4\text{ V}$, $I_{\text{OUT}} = 0\text{ mA}$	—	1.5	5	mA	—
7	Bias current	I_{Bias}	$I_{\text{OUT}} = 0\text{ mA}$	—	0.9	1.5	mA	—
8	Bias current fluctuation to load	$\Delta I_{\text{Bias(L)}}$	$I_{\text{OUT}} = 0\text{ mA to } 100\text{ mA}$	—	3.0	5	mA	—
9	Ripple rejection ratio	RR	$V_{\text{IN}} = 6.74\text{ V to } 8.74\text{ V}$, $f = 120\text{ Hz}$	56	66	—	dB	—

■ Electrical Characteristics (Reference values for design)

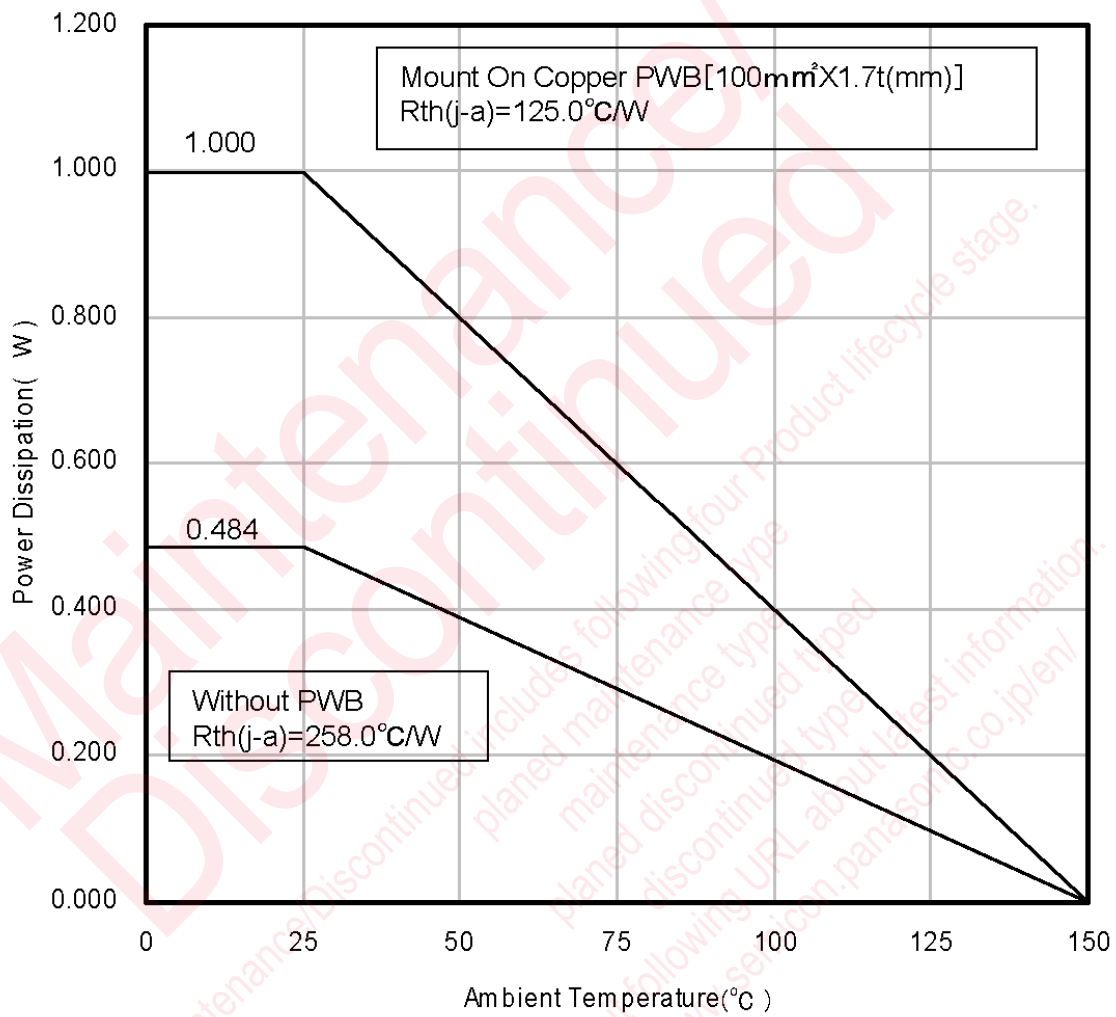
Note) Unless otherwise specified, $T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$, $C_{IN} = 0.33 \mu\text{F}$ and $C_{OUT} = 10 \mu\text{F}$.

The characteristics listed below are reference values for design of the IC and are not guaranteed by inspection.

If a problem does occur related to these characteristics, Panasonic will respond in good faith to user concerns.

B No.	Parameter	Symbol	Conditions	Reference values			Unit	Note
				Min	Typ	Max		
1	Output noise voltage	V_{no}	$V_{IN} = 7.0 \text{ V}$, $I_{OUT} = 50 \text{ mA}$ $f = 10 \text{ Hz to } 100 \text{ kHz}$	—	105	—	μV	—
2	Over heat protection temperature start	$T_{j(TH)}$	$V_I = 7.0 \text{ V}$	—	150	—	$^\circ\text{C}$	—
3	Output voltage temperature coefficient	$\frac{\Delta V_{OUT}}{T_a}$	$V_{IN} = 7.0 \text{ V}$ $T_j = -30^\circ\text{C to } 125^\circ\text{C}$	—	0.40	—	$\text{mV}/^\circ\text{C}$	—

- Technical Data
- $P_D - T_a$ diagram



■ Usage Notes

1. Input short-circuit protection circuit

For the conventional Panasonic 3-pin regulators (such as of the AN80xx series), when DC input pin (pin 3) is short-circuited with GND pin (pin 2) in the normal operation condition, the potential of output pin (pin 1) becomes higher than that of DC input pin and the electric charges which is charged in output capacitor C_{OUT} flows in the input side, having resulted in the breakage of elements.

In the above case, the common silicon diode is connected as shown in the following figure (the dotted line).

However, for the AN77LxxM series, since the protection circuit, which protects the elements from the discharging current, is incorporated in the internal circuit, the protection diode is not required.

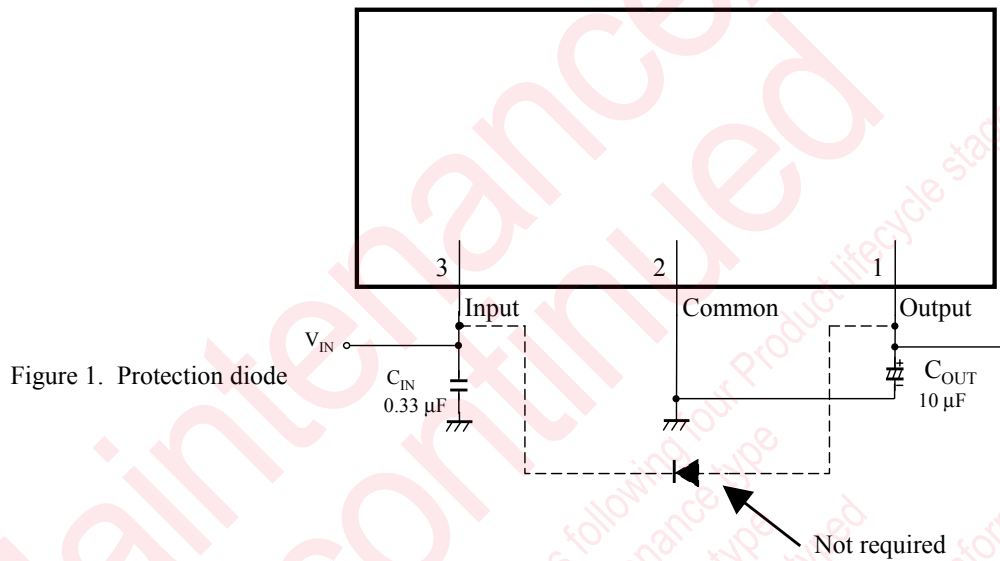


Figure 1. Protection diode

2. Short-circuit between the output pin and the GND pin

Because there is no in-built protection circuit in the AN77LxxM series, they have the drooping characteristics as shown in "Figure 3". When your use under a high voltage happens to cause any short-circuit between the output pin (pin 1) and the GND pin (pin 2), the IC is likely to be broken.

3. Capacitor for external compensation

In order to secure the stability, the capacitor of 10 μF is required in the output side and it should be added as near to output pin (pin 1) and GND pin (pin 2) as possible. When it is used under low temperature, oscillation may occur due to the decrease of the aluminum electrolytic capacitor's capacitance and an increase of ESR. For the AN77LxxM series, it is recommended that the tantalum capacitor or aluminum electrolytic capacitor whose equivalent serial resistance with temperature characteristics within the recommended range specified in "Figure 2" should be used.

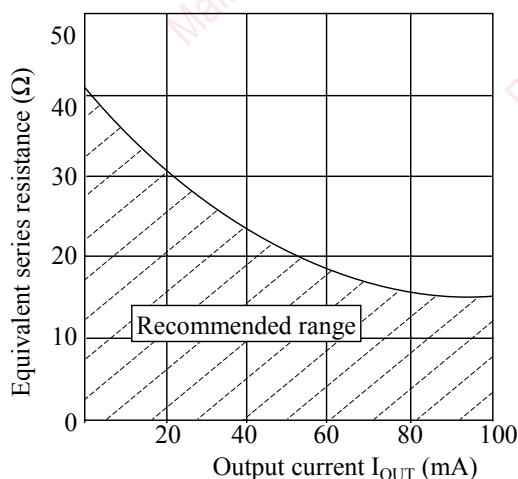


Figure 2. ESR recommended range

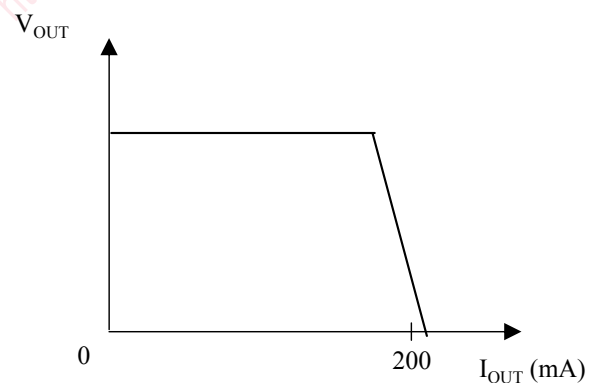


Figure 3. Over current limit control

Request for your special attention and precautions in using the technical information and semiconductors described in this book

- (1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information described in this book.
- (3) The products described in this book are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).
Consult our sales staff in advance for information on the following applications:
 - Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
 - Any applications other than the standard applications intended.
- (4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
 - Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of our company.