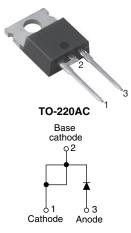


Ultralow V_F Hyperfast Rectifier for Discontinuous Mode PFC, 15 A FRED Pt[®]



VS-15ETL06HN3

PRODUCT SUMMARY					
Package	TO-220AC				
I _{F(AV)}	15 A				
V_{R}	600 V				
V _F at I _F	0.85 V				
t _{rr} typ.	60 ns				
T _J max.	175 °C				
Diode variation	Single die				

FEATURES

- · Hyperfast recovery time
- · Benchmark ultralow forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS COMPLIANT HALOGEN FREE

DESCRIPTION

State of the art, ultralow V_F , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V _{RRM}		600	V			
Average rectified forward current	I _{F(AV)}	T _C = 153 °C	15				
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	250	Α			
Peak repetitive forward current	I _{FM}		30				
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	V		
Forward voltage	V _F	I _F = 15 A	-	0.99	1.05	- V		
		I _F = 15 A, T _J = 150 °C	-	0.85	0.92			
Poverse leekage ourrent	1	$V_R = V_R$ rated	-	0.1	10			
Reverse leakage current I _R		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	15	120	μΑ		
Junction capacitance	C _T	V _R = 600 V	-	20	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nΗ		



DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A}$	$A/\mu s$, $V_R = 30 V$	-	60	-		
Payaraa raaayan tima		$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	190	-		
Reverse recovery time	t _{rr}	T _J = 25 °C	$I_F = 15 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 390 \text{ V}$	-	220	-	ns - A	
		T _J = 125 °C		-	320	-		
Dook roopyon, ourront	,	T _J = 25 °C		-	19	-		
Peak recovery current	IRRM	T _J = 125 °C		-	26	-		
Reverse recovery charge		T _J = 25 °C		-	2.2	-	μC	
	Q_{rr}	T _J = 125 °C		-	4.3	-		

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C		
Thermal resistance, junction to case	R _{thJC}		-	1.1	1.4			
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W		
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-			
Maiabt			-	2.0	-	g		
Weight			-	0.07	-	oz.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Marking device		Case style TO-220AC	15ETL06H					

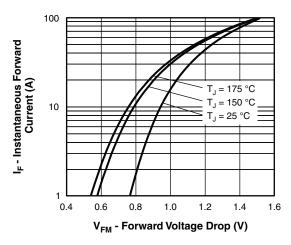


Fig. 1 - Maximum Forward Voltage Drop Characteristics

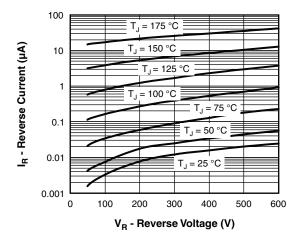


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



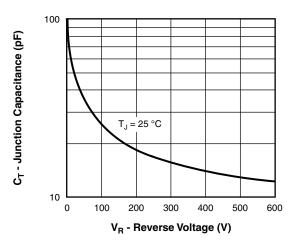


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

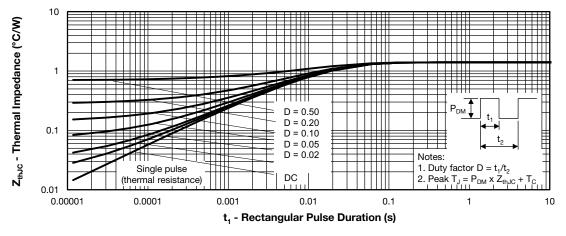


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

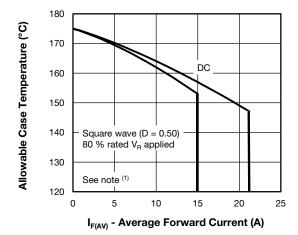


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

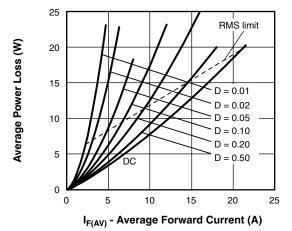


Fig. 6 - Forward Power Loss Characteristics



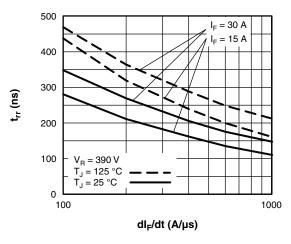


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

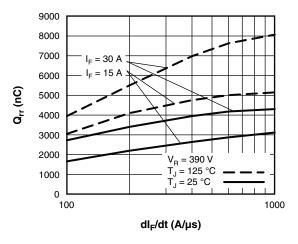
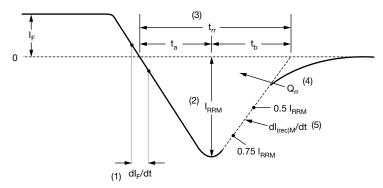


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

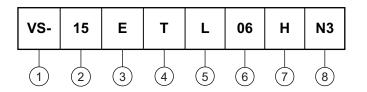
(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (15 = 15 A)

3 - E = single diode

4 - T = TO-220

L = ultralow V_F hyperfast recovery

6 - Voltage rating (06 = 600 V)

7 - H = AEC-Q101 qualified

8 - Environmental digit:

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

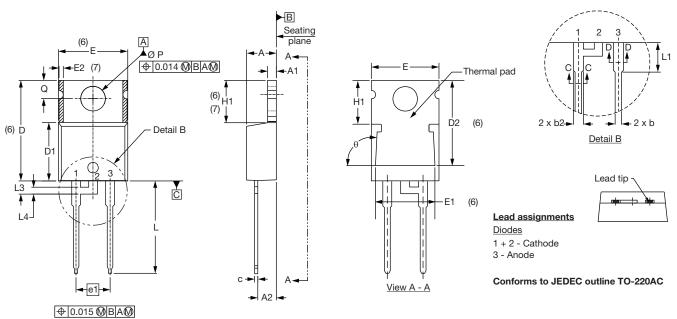
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-15ETL06HN3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95221				
Part marking information	www.vishay.com/doc?95068				
SPICE model	www.vishay.com/doc?96051				



TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	TERS INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIM	IETERS	INCHES		NOTES	
STINIBUL	MIN.	MAX.	MIN.	MAX.	HOILS	
E1	6.86	8.89	0.270	0.350	6	
E2	-	0.76	-	0.030	7	
е	2.41	2.67	0.095	0.105		
e1	4.88	5.28	0.192	0.208		
H1	6.09	6.48	0.240	0.255	6, 7	
L	13.52	14.02	0.532	0.552		
L1	3.32	3.82	0.131	0.150	2	
L3	1.78	2.13	0.070	0.084		
L4	0.76	1.27	0.030	0.050	2	
ØΡ	3.54	3.73	0.139	0.147		
Q	2.60	3.00	0.102	0.118		
θ	90° t	o 93°	90° to 93°			

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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Vishay

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